



#7A/4-5-04
v. Jones

PATENT
PD-97-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: DAVID H. MILLER ET AL
Serial No.: 09/838,610
Filed: April 19, 2001
For: CODE-PROGRAMMABLE FIELD-
PROGRAMMABLE ARCHITECTURALLY-SYSTOLIC
REED-SOLOMON BCH ERROR CORRECTION
DECODER INTEGRATED CIRCUIT AND ERROR
CORRECTION DECODING METHOD

Date: March 25, 2004

Group Art Unit: 2133

Examiner: Joseph D. Torres

AMENDMENT

Commissioner of Patents and Trademarks
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed March 16, 2004, please amend the above-identified patent application as follows. This Amendment is submitted in "Revised Format".

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MAR 30 2004

Technology Center 2100